



Product Specification

SPECIFICATION FOR APPROVAL

()	Prelimina	ry Spe	ecification
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(\spadesuit) Final Specification

Title		15.6" HD TFT LC	D
Customer	F.IT	SUPPLIER	LG Display Co., Ltd.

Customer	FJT
MODEL	

SUPPLIER	LG Display Co., Ltd.
*MODEL	LP156WH3
Suffix	TLS1

^{*}When you obtain standard approval, please use the above model name without suffix

APPROVED BY	SIGNATURE
Please return 1 copy for you your signature and commen	

APPROVED BY	SIGNATURE				
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Products Engineering Dept. LG Display Co., Ltd					

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RECORD OF REVISIONS

Revision No	Revision Date	Page	Description	EDID ver
0.0	Jul. 06. 2012	-	First Draft (Preliminary Specification)	-
0.1	Aug 01. 2012	24-27	Update EDID Data	
0.2	Sep 17. 2012	14	Update Color Gamut	0.1
1.0	Oct 04. 2012	-	Final Version	1.0

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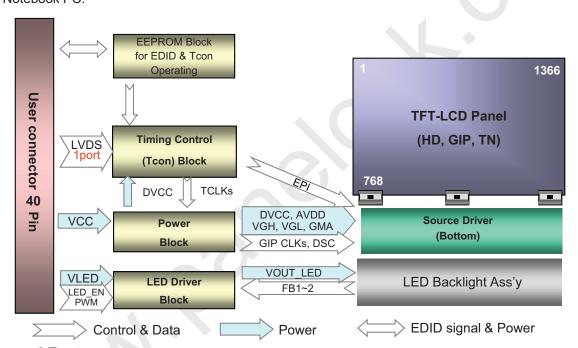




Product Specification

1. General Description

The LP156WH3 is a Color Active Matrix Liquid Crystal Display with an integral LED backlight system. The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive type display operating in the normally white mode. This TFT-LCD has 15.6 inches diagonally measured active display area with HD resolution (1366 horizontal by 768 vertical pixel array). Each pixel is divided into Red, Green and Blue subpixels or dots which are arranged in vertical stripes. Gray scale or the brightness of the sub-pixel color is determined with a 6-bit gray scale signal for each dot, thus, presenting a palette of more than 262,144 colors. The LP156WH3 has been designed to apply the interface method that enables low power, high speed, low EMI. The LP156WH3 is intended to support applications where thin thickness, low power are critical factors and graphic displays are important. In combination with the vertical arrangement of the subpixels, the LP156WH3 characteristics provide an excellent flat display for office automation products such as Notebook PC.



General Features

Active Screen Size	15.6 inches diagonal
Outline Dimension	359.5(H, Typ.) × 217.2(V, Typ.) × 3.8(D, Max.) [mm] (with PCB Board)
Pixel Pitch	0.252mm X 0.252 mm
Pixel Format	1366 horiz. by 768 vert. Pixels RGB strip arrangement
Color Depth	6-bit, 262,144 colors
Luminance, White	200 cd/m²(Typ.)
Power Consumption	Total 3.2W (Typ.) Logic : 0.7W (Typ.@ Mosaic), B/L : 2.5W (Typ.@ VLED 12V)
Weight	400g (Max.)
Display Operating Mode	Transmissive mode, normally white
Surface Treatment	Glare treatment (3H) of the front Polarizer
RoHS Compliance	Yes
BFR/PVC/As Free	Yes for all

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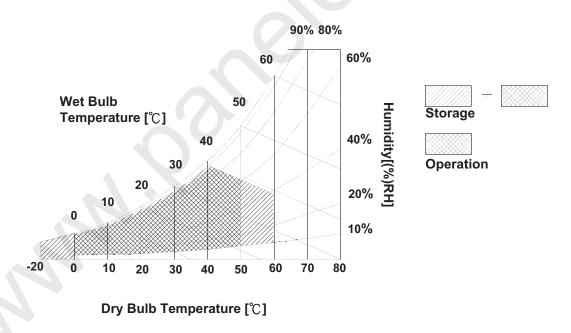
2. Absolute Maximum Ratings

The following are maximum values which, if exceeded, may cause faulty operation or damage to the unit.

Table 1. ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Val	ues	Units	Notes	
Farameter	Syllibol	Min		Offics	Notes	
Power Input Voltage	VCC	-0.3	4.0	Vdc	at 25 ± 5°C	
Operating Temperature	Тор	0	50	°C	1	
Storage Temperature	Hst	-20	60	°C	1	
Operating Ambient Humidity	Нор	10	90	%RH	1	
Storage Humidity	Hst	10	90	%RH	1	

Note: 1. Temperature and relative humidity range are shown in the figure below. Wet bulb temperature should be 39°C Max, and no condensation of water.







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3. Electrical Specifications

3-1. Electrical Characteristics

The LP156WH3 requires two power inputs. The first logic is employed to power the LCD electronics and to drive the TFT array and liquid crystal. The second backlight is the input about LED BL with LED Driver.

Table 2. ELECTRICAL CHARACTERISTICS

Parameter		Complete I		Values		Unit	
		Symbol	Min	Тур	Max	Unit	Notes
LOGIC:							
Power Supply Input Voltage		Vcc	3.0	3.3	3.6	V	1
Power Supply Input Current	Mosaic	Icc	-	210	245	mA	2
Power Consumption		Pcc	-	0.7	0.8	W	2
Power Supply Inrush Current		Icc_p	-	<u>)</u> -	1500	mA	3
LVDS Impedance		ZLVDS	90	100	110	Ω	4
BACKLIGHT : (with LED Driver	r)						
LED Power Input Voltage		VLED	7.0	12.0	21.0	V	5
LED Power Input Current		ILED	-	210	230	mA	6
LED Power Consumption		PLED	-	2.5	2.8	W	6
LED Power Inrush Current		ILED_P	-	-	2000	mA	7
PWM Duty Ratio			5	-	100	%	8
PWM Jitter		-	0	_	0.2	%	9
PWM Impedance		Zpwm	20	40	60	kΩ	
PWM Frequency		FPWM	200	-	1000	Hz	10
PWM High Level Voltage		V _{PWM_H}	3.0	-	3.6	V	
PWM Low Level Voltage		V_{PWM_L}	0	-	0.3	V	
LED_EN Impedance		Zpwm	20	40	60	kΩ	
LED_EN High Voltage		VLED_EN_H	3.0	-	3.6	V	
LED_EN Low Voltage		VLED_EN_L	0	-	0.3	V	
Life Time			12,000	-	-	Hrs	11

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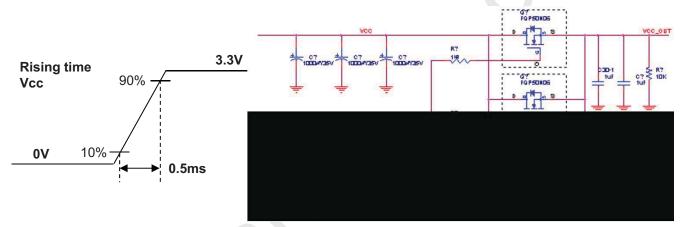




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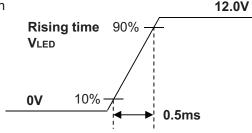
Note)

- 1. The measuring position is the connector of LCM and the test conditions are under 25℃, fv = 60Hz, Black pattern.
- 2. The specified Icc current and power consumption are under the Vcc = 3.3V, $25^{\circ}C$, fv = 60Hz condition and Mosaic pattern.
- This Spec. is the max load condition for the cable impedance designing.
- The below figures are the measuring Vcc condition and the Vcc control block LGD used.The Vcc condition is same as the minimum of T1 at Power on sequence.



- 4. This impedance value is needed for proper display and measured form LVDS Tx to the mating connector.
- 5. The measuring position is the connector of LCM and the test conditions are under 25°C.
- 6. The current and power consumption with LED Driver are under the Vled = 12.0V, 25° C, Dimming of Max luminance and White pattern with the normal frame frequency operated(60Hz).
- 7. The below figures are the measuring VIed condition and the VIed control block LGD used.

VLED control block is same with Vcc control block.



- 8. The operation of LED Driver below minimum dimming ratio may cause flickering or reliability issue.
- 9. If Jitter of PWM is bigger than maximum, it may induce flickering.
- 10. This Spec. is not effective at 100% dimming ratio as an exception because it has DC level equivalent to 0Hz. In spite of acceptable range as defined, the PWM Frequency should be fixed and stable for more consistent brightness control at any specific level desired.
- 11. The life time is determined as the time at which brightness of LCD is 50% compare to that of minimum value specified in table 7. under general user condition.

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3-2. Interface Connections

This LCD employs two interface connections, a 40 pin connector used for the module electronics interface and the other connector used for the integral backlight system.

Table 3. MODULE CONNECTOR PIN CONFIGURATION (CN1)

Pin	Symbol	Description	Notes
1	NC	No Connection	[Interface Chip]
2	VCC	LCD Logic and driver power (3.3V Typ.)	1. LCD :
3	VCC	LCD Logic and driver power (3.3V Typ.)	SiW, SW0662(LCD Controller)
4	V EEDID	DDC Power (3.3V)	Including LVDS Receiver.
5	NC	No Connection	2. System : SiW LVDSRx or equivalent
6	Clk EEDID	DDC Clock	* Pin to Pin compatible with LVDS
7	DATA EEDID	DDC Data	
8	ORX0-	Negative LVDS differential data input	
9	ORX0+	Positive LVDS differential data input	
10	GND	High Speed Ground	[Connector]
11	ORX1-	Negative LVDS differential data input	Hirose KN38-40S-0.5H
12	ORX1+	Positive LVDS differential data input	
13	GND	High Speed Ground	
14	ORX2-	Negative LVDS differential data input	[Connector pin arrangement]
15	ORX2+	Positive LVDS differential data input	Locumentor but arrangements
16	GND	High Speed Ground	
17	ORXC-	Negative LVDS differential clock input	
18	ORXC+	Positive LVDS differential clock input	
19	GND	High Speed Ground	40 1
20	NC	No Connection	1
21	NC	No Connection	
22	GND	High Speed Ground	
23	NC	No Connection	
24	NC	No Connection	
25	GND	High Speed Ground	
26	NC	No Connection	
27	NC	No Connection	
28	GND	High Speed Ground	
29	NC	No Connection	
30	NC	No Connection	
31	GND	LED Backlight Ground	
32	GND	LED Backlight Ground	
33	GND	LED Backlight Ground	
34	NC	No Connection	
35	PWM	System PWM Signal input for dimming	
36	LED_EN	LED Backlight On/Off	
37	NC	No Connection	
38	VLED	LED Backlight Power (7V-21V)	
39	VLED	LED Backlight Power (7V-21V)	
40	VLED	LED Backlight Power (7V-21V)	

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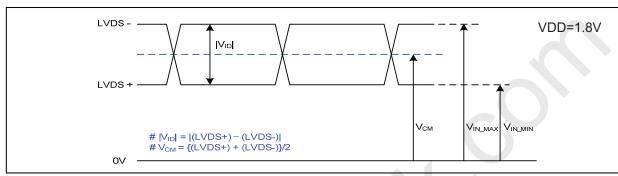




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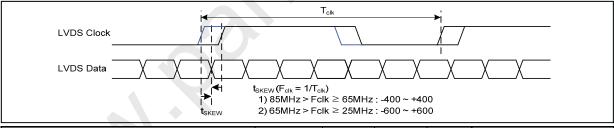
3-3. LVDS Signal Timing Specifications

3-3-1. DC Specification



Description	Symbol	Min	Тур	Max	Unit	Notes
LVDS Differential Voltage	V _{ID}	100	-	600	mV	-
LVDS Common mode Voltage	V_{CM}	V _{ID} /2	1.2	VDD- V _{ID} /2	V	-
LVDS Input Voltage Range	V_{IN}	0.3	_	VDD	V	-

3-3-2. AC Specification



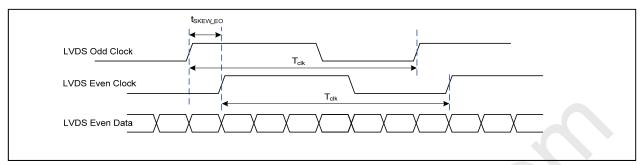
Description	Symbol	Min	Max	Unit	Notes
LVDS Clock to Data Skow Margin	t _{skew}	- 400	+ 400	ps	85MHz > Fclk ≥ 65MHz
LVDS Clock to Data Skew Margin	t _{skew}	- 600	+ 600	ps	65MHz > Fclk ≥ 25MHz
LVDS Clock to Clock Skew Margin (Even to Odd)	t _{skew_eo}	- 1/7	+ 1/7	T _{clk}	-
Maximum deviation of input clock frequency during SSC	F _{DEV}	-	± 3	%	-
Maximum modulation frequency of input clock during SSC	F _{MOD}	-	200	KHz	-

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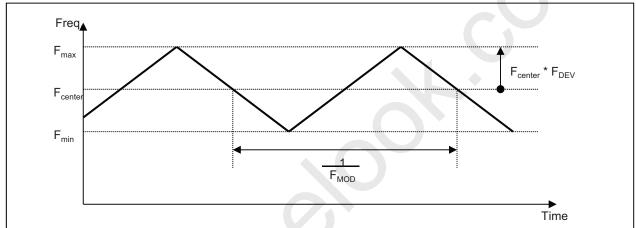




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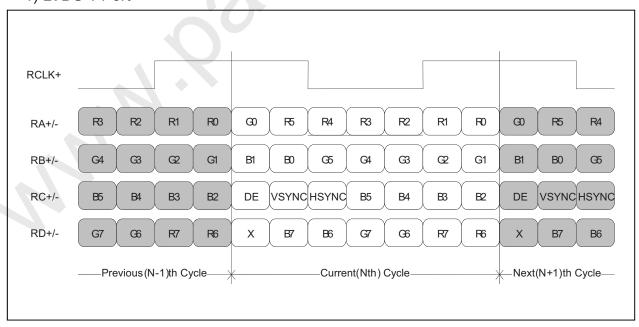
< Clock skew margin between channel >



< Spread Spectrum >

3-3-3. Data Format

1) LVDS 1 Port



< LVDS Data Format >

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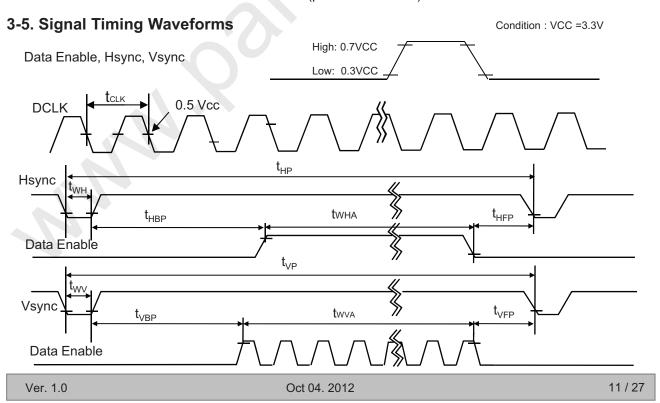
3-4. Signal Timing Specifications

This is the signal timing required at the input of the User connector. All of the interface signal timing should be satisfied with the following specifications and specifications of LVDS Tx/Rx for its proper operation.

Table 4. TIMING TABLE

		Table 4. I	IIVIIIVG 17	ADLL			
ITEM	Symbol		Min	Тур	Max	Unit	Note
DCLK	Frequency	f _{CLK}	-	69.3	-	MHz	4
	Period	t _{HP}	1422	1478	1542		
Hsync	Width	t _{WH}	24	32	48	tCLK	
	Width-Active	t _{WHA}	1366	1366	1366		
	Period	t _{VP}	775	782	790		
Vsync	Width	t _{WV}	2	4	5	tHP	
	Width-Active	t _{WVA}	768	768	768		
	Horizontal back porch	t _{HBP}	16	48	80	+OL 14	
Data	Horizontal front porch	t _{HFP}	16	32	48	tCLK	
Enable	Vertical back porch	t_{VBP}	4	8	14	4110	
	Vertical front porch	t_{VFP}	1	2	3	tHP	

Appendix) all reliabilities are specified for timing specification based on refresh rate of 60Hz. However, LP156WH3 has a good actual performance even at lower refresh rate (e.g. 40Hz or 50Hz) for power saving Mode, whereas LP156WH3 is secured only for function under lower refresh rate. 60Hz at Normal mode, 50Hz, 40Hz at Power save mode. Don't care Flicker level (power save mode).







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3-6. Color Input Data Reference

The brightness of each primary color (red,green and blue) is based on the 6-bit gray scale data input for the color; the higher the binary input, the brighter the color. The table below provides a reference for color versus data input.

Table 5. COLOR DATA REFERENCE

									Inp	out Co	olor D	ata							
	Color			RI	ΞD					GRE	EEN					BL	UE		
	50101	MSE	3				LSB	MSI	3				LSB	MSE	3				LSB
		R 5	R 4	R 3	R 2	R 1	R 0	G 5	G 4	G 3	G 2	G 1	G 0	B 5	B 4	B 3	B 2	B 1	B 0
	Black	0	0			0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	.1	. 1	1	1	0	0		0	0	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	1	1	1	1	1		0	0	0	0	0	0
Basic	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
Color	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	RED (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (01)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
RED																			
	RED (62)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (01)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
GREEN		.			 														
	GREEN (62)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	GREEN (63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	BLUE (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE (01)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
BLUE					 						 						 		
	BLUE (62)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
	BLUE (63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1

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3-7. Power Sequence

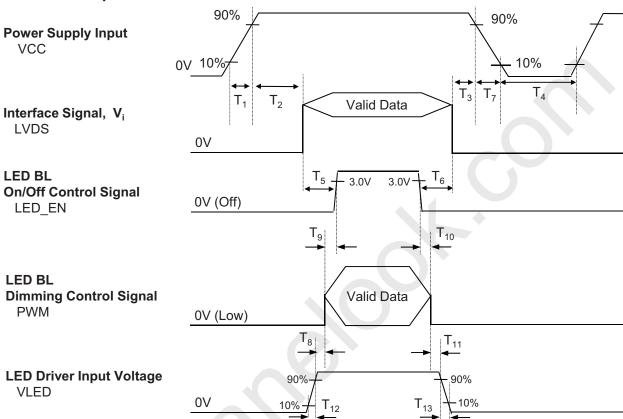


Table 6. POWER SEQUENCE TABLE

Logic		Value		Linita	LED		Units		
Parameter	Min.	Тур.	Max.	Units	Parameter	Min.	Тур.	Max.	Units
T ₁	0.5		10	ms	T ₈	10	1	-	ms
T ₂	0	-	50	ms	T ₉	0	1	-	ms
T ₃	0	ı	50	ms	T ₁₀	0	1	-	ms
T ₄	400	ı	-	ms	T ₁₁	10	1	-	ms
T ₅	200	ı	-	ms	T ₁₂	0.5	1	-	ms
T ₆	200	-	-	ms	T ₁₃	0	-	5000	ms
T ₇	3	-	10	ms					

Note)

- 1. Do not insert the mating cable when system turn on.
- 2. Valid Data have to meet "3-3. LVDS Signal Timing Specifications"
- 3. LVDS, LED_EN and PWM need to be on pull-down condition on invalid status.
- 4. LGD recommend the rising sequence of VLED after the Vcc and valid status of LVDS turn on.

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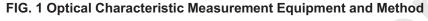


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4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' and stable for approximately 20 minutes in a dark environment at 25°C. The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of Φ and Θ equal to 0° .

FIG. 1 presents additional information concerning the measurement equipment and method.



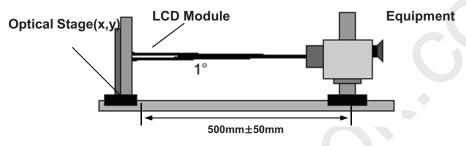


Table 7. OPTICAL CHARACTERISTICS

Ta=25°C, VCC=3.3V, fv=60Hz, f_{CLK}= 69.3MHz

			I :	a=25°C, VC0	ر=3.3V, ™	=60Hz, f _{CLK} = 69.3MHz
Parameter	Symbol		Values		Units	Notes
i didilletei	Symbol	Min	Тур	Max	Offics	Notes
Contrast Ratio	CR	400	500	-		1
Surface Luminance, white	L_WH	170	200	-	cd/m ²	2
Luminance Variation	δ _{WHITE (5P)}	-	1.2	1.4		3
Luminance variation	δ _{WHITE(13P)}		1.4	1.6		3
Response Time	Tr_{R} Tr_{D}	-	16	25	ms	4
Color Gamut (CIE1931)		-	45	-	%	
Color Coordinates						
RED	RX	0.548	0.578	0.608		
	RY	0.314	0.344	0.374		
GREEN	GX	0.307	0.337	0.367		
	GY	0.541	0.571	0.601		
BLUE	BX	0.129	0.159	0.189		
	BY	0.090	0.120	0.150		
WHITE	WX	0.283	0.313	0.343		
	WY	0.299	0.329	0.359		
Viewing Angle						5
x axis, right(⊕=0°)	Θr	40	-	-	degree	
x axis, left (Ф=180°)	Θl	40	-	-	degree	
y axis, up (Φ=90°)	Θu	10	-	-	degree	
y axis, down (⊕=270°)	Θd	30	-	-	degree	
Gray Scale						6

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Note)

1. Contrast Ratio(CR) is defined mathematically as

2. Surface luminance is the average of 5 point across the LCD surface 50cm from the surface with all pixels displaying white. For more information see FIG 1.

$$LWH = Average(L1, L2, ... L5)$$

3. The variation in surface luminance , The panel total variation (δ WHITE) is determined by measuring LN at each test position 1 through 13 and then defined as following numerical formula. For more information see FIG 2.

$$\delta \text{ WHITE (13P)} = \frac{\text{Maximum (L1,L2, ... L13)}}{\text{Minimum (L1,L2, ... L13)}} \delta \text{ WHITE (5P)} = \frac{\text{Maximum(L1,L2, ... L5)}}{\text{Minimum(L1,L2, ... L5)}}$$

- 4. Response time is the time required for the display to transition from white to black (rise time, TrR) and from black to white(Decay Time, TrD). For additional information see FIG 3.
- 5. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG 4.
- 6. Gray scale specification

' fV = 60Hz

Gray Level	Luminance [%] (Typ)
LO	0.18
L7	1.52
L15	5.65
L23	14.2
L31	24.5
L39	40.2
L47	57.7
L55	77.9
L63	100





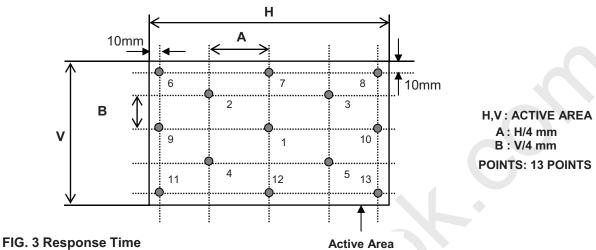
Global LCD Panel Exchange Center

LP156WH3 Liquid Crystal Display

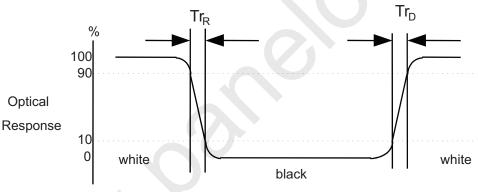
Product Specification

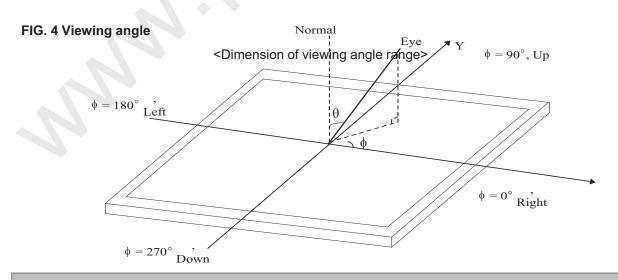
FIG. 2 Luminance

<Measuring point for Average Luminance & measuring point for Luminance variation>



The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white".









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5. Mechanical Characteristics

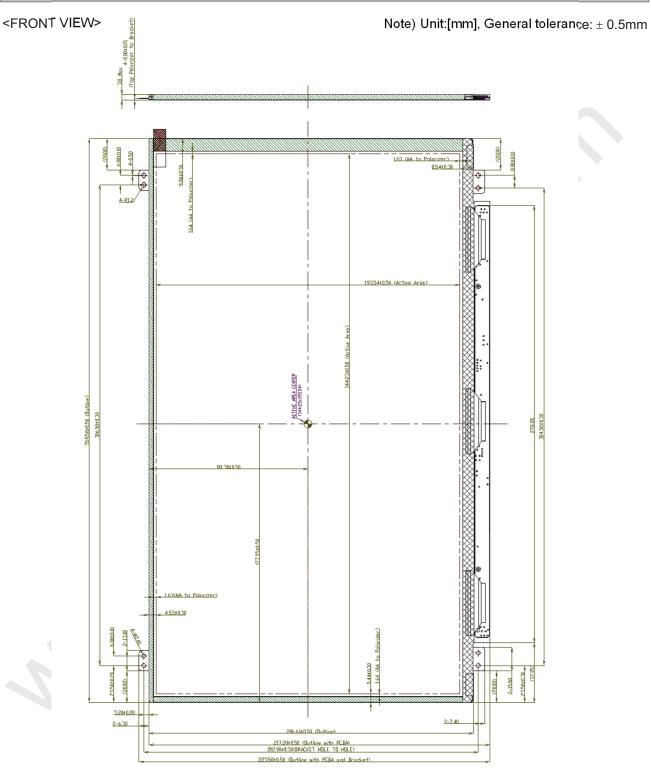
The contents provide general mechanical characteristics for the model LP156WH3. In addition the figures in the next page are detailed mechanical drawing of the LCD.

	Horizontal	359.5 ± 0.5mm					
Outline Dimension	Vertical	217.2 ± 0.5mm					
	Thickness	3.8mm (max)					
Bezel Area	Horizontal	347.5 ± 0.5mm					
bezei Alea	Vertical	196.8 ± 0.5mm					
Active Dieplay Area	Horizontal	344.23 mm					
Active Display Area	Vertical	193.54 mm					
Weight	400g (Max.)						
Surface Treatment	Hard Coating(3H), Glare treatment of the front polarizer						





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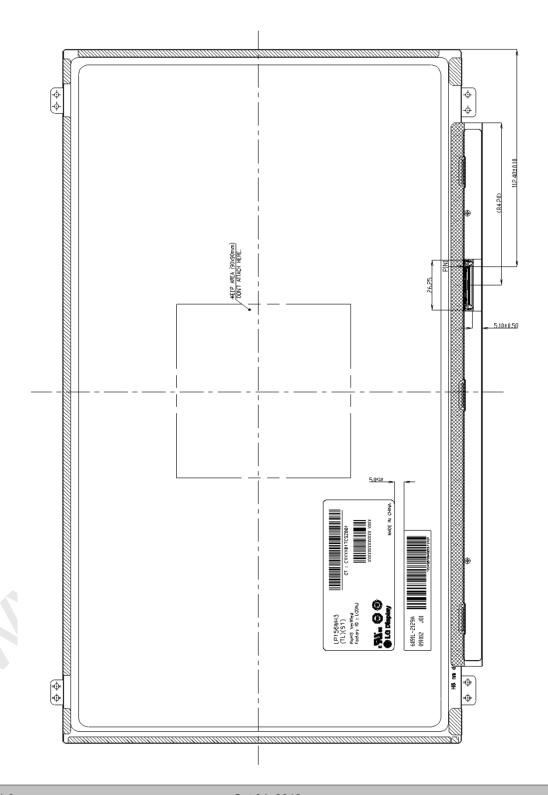




Product Specification

<REAR VIEW>

Note) Unit:[mm], General tolerance: \pm 0.5mm



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6. Reliability

Environment test condition

No.	Test Item	Conditions					
1	High temperature storage test	Ta= 60°C, 240h					
2	Low temperature storage test	Ta= -20°C, 240h					
3	High temperature operation test	Ta= 50°C, 50%RH, 240h					
4	Low temperature operation test	Ta= 0°C, 240h					
5	Vibration test (non-operating)	Sine wave, 5 ~ 150Hz, 1.5G, 0.37oct/min 3 axis, 30min/axis					
6	Shock test (non-operating)	 No functional or cosmetic defects following a shock to all 6 sides delivering at least 180 G in a half sine pulse no longer than 2 ms to the display module No functional defects following a shock delivering at least 200 g in a half sine pulse no longer than 2 ms to each of 6 sides. Each of the 6 sides will be shock tested with one each display, for a total of 6 displays 					
7	Altitude operating storage / shipment	0 ~ 10,000 feet (3,048m) 24Hr 0 ~ 40,000 feet (12,192m) 24Hr					

[{] Result Evaluation Criteria }

There should be no change which might affect the practical display function when the display quality test is conducted under normal operating condition.





Product Specification

7. International Standards

7-1. Safety

- a) UL 60950-1, Underwriters Laboratories Inc.
 Information Technology Equipment Safety Part 1: General Requirements.
- b) CAN/CSA C22.2 No.60950-1-07, Canadian Standards Association.
 Information Technology Equipment Safety Part 1: General Requirements.
- c) EN 60950-1, European Committee for Electrotechnical Standardization (CENELEC). Information Technology Equipment Safety Part 1 : General Requirements.
- d) IEC 60950-1, The International Electrotechnical Commission (IEC). Information Technology Equipment Safety Part 1 : General Requirements.

7-2. EMC

- a) ANSI C63.4 "American National Standard for Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electronic Equipment in the Range of 9 kHz to 40 GHz." American National Standards Institute (ANSI), 2003.
- b) CISPR 22 "Information technology equipment Radio disturbance characteristics Limit and methods of measurement." International Special Committee on Radio Interference (CISPR), 2005.
- c) CISPR 13 "Sound and television broadcast receivers and associated equipment Radio disturbance characteristics Limits and method of measurement." International Special Committee on Radio Interference (CISPR), 2006.

7-3. Environment

a) RoHS, Directive 2002/95/EC of the European Parliament and of the council of 27 January 2003



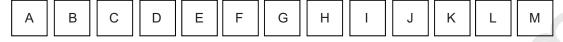


Product Specification

8. Packing

8-1. Designation of Lot Mark

a) Lot Mark



 $A,B,C:SIZE(INCH) \\ D:YEAR$

E: MONTH $F \sim M$: SERIAL NO.

Note

1. YEAR

Year	2011	2012	2013	2014	2015	2016	2017	2018	2019	2020
Mark	Α	В	С	D	Е	F	G	Н	J	K

2. MONTH

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Mark	1	2	3	4	5	6	7	8	9	Α	В	С

b) Location of Lot Mark

Serial No. is printed on the label. The label is attached to the backside of the LCD module. This is subject to change without prior notice.

8-2. Packing Form

a) Package quantity in one box : 20pcs

b) Box Size : 478mm X 365mm X 328mm





Product Specification

9. PRECAUTIONS

Please pay attention to the followings when you use this TFT LCD module.

9-1. MOUNTING PRECAUTIONS

- (1) You must mount a module using holes arranged in four corners or four sides.
- (2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to to h e module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment. Do not touch the surface of polarizer for bare hand or greasy cloth.(Some cosmetics are detrimental to the polarizer.)
- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.
- (10) When handling the LCD module, it needs to handle with care not to give mechanical stress to the PCB and Mounting Hole area."

9-2. OPERATING PRECAUTIONS

- (1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage : $V=\pm\ 200mV(Over\ and\ under\ shoot\ voltage)$
- (2) Response time depends on the temperature.(In lower temperature, it becomes longer.)
- (3) Brightness depends on the temperature. (In lower temperature, it becomes lower.) And in lower temperature, response time(required time that brightness is stable after turned on) becomes longer.
- (4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference.





Product Specification

9-3. ELECTROSTATIC DISCHARGE CONTROL

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

9-4. PRECAUTIONS FOR STRONG LIGHT EXPOSURE

Strong light exposure causes degradation of polarizer and color filter.

9-5. STORAGE

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object.

 It is recommended that they be stored in the container in which they were shipped.

9-6. HANDLING PRECAUTIONS FOR PROTECTION FILM

- (1) When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
- (2) The protection film is attached to the polarizer with a small amount of glue. If some stress is applied to rub the protection film against the polarizer during the time you peel off the film, the glue is apt to remain on the polarizer.
 - Please carefully peel off the protection film without rubbing it against the polarizer.
- (3) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the polarizer after the protection film is peeled off.
- (4) You can remove the glue easily. When the glue remains on the polarizer surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.







Product Specification

APPENDIX A. Enhanced Extended Display Identification Data (EEDID™) 1/3

	Byte	Byte	Field Name and Comments	Value	Value
Header	(Dec)	(Hex)		(Hex)	(Bin)
	0	00	Header Header	00 FF	00000000
	2	02	Header	FF	11111111
	3	03	Header	FF	11111111
	4	04	Header	FF	11111111
	5	05	Header	FF	11111111
	6	06	Header	FF	11111111
	7	07	Header	00	00000000
D	8	08	EISA manufacture code (3 Character ID) LGD	30	00110000
EDID	9	09	EISA manufacture code (Compressed ASCII)	E4	11100100
E	10	0A	Panel Supplier Reserved - Product Code 032Eh	2E	00101110 00000011
	11 12	0B 0C	(Hex. LSB first) LCD Module Serial No - Preferred but Optional ("0" If not used)	00	00000011
T M	13	0D	LCD Module Serial No - Preferred but Optional ("0" If not used)	00	00000000
roduct Version	14	0E	LCD Module Serial No - Preferred but Optional ("0" If not used)	00	00000000
oo,	15	0F	LCD Module Serial No - Preferred but Optional ("0" If not used)	00	00000000
Vendor/Product Versio	16	10	Week of Manufacture 00 weeks	00	00000000
do	17	11	Year of Manufacture 2012 years	16	00010110
, en	18	12	EDID structure version # = 1	01	00000001
1	19	13	EDID revision # = 3	03	00000011
r _a	20	14	Video input Definition = Digital signal	80	10000000
Display Parameters	21	15	Max H image size (Rounded cm) = 35 cm	23	00100011
nd nd	22	16	Max V image size (Rounded cm) = 19 cm	13	00010011
Display aramete	23	17	Display gamma = (gamma*100)-100 = Example:(2.2*100)-100=120 = 2.2 Gamma	78	01111000
l Pa	24	18	Feature Support (no_DPMS, no_Active Off/Very Low Power, RGB color display,	0A	00001010
	25	10	Timing BLK 1,no GTF)	0.5	00000101
	25	19	Red/Green Low Bits (RxRy/GxGy)	05	00000101
3	26	1A	Blue/White Low Bits (BxBy/WxWy)	F5	11110101
na	27	1B	Red X Rx = 0.578	94	10010100
rdi	28	1C	Red Y $Ry = 0.344$	58	01011000
00	29	1D	Green X $Gx = 0.337$	56	01010110
Ċ	30	1E	Green Y $Gy = 0.571$	92	10010010
lo _r	31	1F	Blue X $Bx = 0.159$	28	00101000
l Co	32	20	Blue Y By = 0.120	1E	00011110
Punel Color Coordinates	33	21	White X $Wx = 0.313$	50	01010000
7	34	22	White Y $Wy = 0.329$	54	01010100
· 2 ·	35	23	Established timing 1 (00h if not used)	00	00000000
LSIA blish ed Timi	36	24	Established timing 2 (00h if not used)	00	00000000
1 P	37	25	Manufacturer's timings (00h if not used)	00	00000000
	38	26	Standard timing ID1 (01h if not used)	01	00000001
	39	27	Standard timing ID1 (01h if not used)	01	00000001
	40	28	Standard timing ID2 (01h if not used)	01	00000001
	41	29	Standard timing ID2 (01h if not used)	01	00000001
n'n	42	2A	Standard timing ID3 (01h if not used)	01	00000001
28	43	2B	Standard timing ID3 (01h if not used) Standard timing ID4 (01h if not used)	01	00000001
Standard Timing ID	44	2C 2D	Standard timing ID4 (01h if not used) Standard timing ID4 (01h if not used)	01 01	00000001
	46	2D 2E	Standard timing ID4 (01h it not used) Standard timing ID5 (01h if not used)	01	00000001
	47	2F	Standard timing ID5 (01h if not used) Standard timing ID5 (01h if not used)	01	00000001
	48	30	Standard timing ID6 (01h if not used)	01	00000001
	49	31	Standard timing ID6 (01h if not used)	01	00000001
	50	32	Standard timing ID7 (01h if not used)	01	00000001
	51	33	Standard timing ID7 (01h if not used)	01	00000001
	52	34	Standard timing ID8 (01h if not used)	01	00000001
	53	35	Standard timing ID8 (01h if not used)	01	00000001

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Product Specification

APPENDIX A. Enhanced Extended Display Identification Data (EEDID™) 2/3

	Byte	Byte	E' II N	Value	Value
	(Dec)	(Hex)	Field Name and Comments	(Hex)	(Bin)
	54	36	Pixel Clock/10,000 (LSB) 69.3 MHz	12	00010010
	55	37	Pixel Clock/10,000 (MSB)	1B	00011011
	56	38	Horizontal Active (lower 8 bits) 1366 Pix	56	01010110
	57	39	Horizontal Blanking(Thp-HA) (lower 8 bits) 112 Pix	70	01110000
	58	3A	Horizontal Active / Horizontal Blanking(Thp-HA) (upper 4:4bits)	50	01010000
#	59	3B	Vertical Avtive 768	00	00000000
5	60	3C	Vertical Blanking (Tvp-HA) (DE Blanking typ.for DE only panels) 14 Line	0E	00001110
dj.	61	3D	Vertical Active : Vertical Blanking (Tvp-HA) (upper 4:4bits)	30	00110000
Timing Descriptor #1	62	3E	Horizontal Sync. Offset (Thfp) 32 Pi	20	00100000
	63	3F	Horizontal Sync Pulse Width (HSPW) 32 Pix	20	00100000
	64	40	Vertical Sync Offset(Tvfp): Sync Width (VSPW) 2 Lines: 4 Line	24	00100100
	65	41	Horizontal Vertical Sync Offset/Width (upper 2bits)	00	00000000
Ë	66	42	Horizontal Image Size (mm) 345	59	01011001
	67	43	Vertical Image Size (mm) 194	C2	11000010
	68	44	Horizontal Image Size / Vertical Image Size	10	00010000
	69	45	Horizontal Border = 0 (Zero for Notebook LCD)	00	00000000
	70	46	Vertical Border = 0 (Zero for Notebook LCD)	00	00000000
	71	47	Non-Interlace, Normal display, no stereo, Digital Separate (Vsync_NEG, Hsync_NEG), DE only note: LSB is set to 'l' if panel is DE-timing only. H/V can be ignored.	19	00011001
	72	48	Flag	00	00000000
	73	49	Flag	00	00000000
	74	4A	Flag	00	00000000
	75	4B	Data Type Tag (Descriptor Defined by manufacturer)	00	00000000
	76	4C	Flag	00	00000000
#	77	4D	Descriptor Defined by manufacturer	00	00000000
j.	78	4E	Descriptor Defined by manufacturer	00	00000000
ip	79	4F	Descriptor Defined by manufacturer	00	00000000
Iining Descriptor #2	80	50	Descriptor Defined by manufacturer	00	00000000
<u> </u>	81	51	Descriptor Defined by manufacturer	00	00000000
20	82	52	Descriptor Defined by manufacturer	00	00000000
į	83	53	Descriptor Defined by manufacturer	00	00000000
E	84	54	Descriptor Defined by manufacturer	00	00000000
	85	55	Descriptor Defined by manufacturer	00	00000000
	86	56	Descriptor Defined by manufacturer	00	00000000
	87	57	Descriptor Defined by manufacturer	00	00000000
	88	58	Descriptor Defined by manufacturer	00	00000000
	89	59	Descriptor Defined by manufacturer	00	00000000
	90	5A	Flag	00	00000000
	91	5B	Flag	00	00000000
	92	5C	Flag	00	00000000
	93	5D	Data Type Tag (ASCII String)	FE	11111110
A.	94	5E	Flag	00	00000000
#	95	5F	ASCII String L	4C	01001100
Tining Descriptor	96	60	ASCII String G	47	01000111
	97	61	ASCII String	20	00100000
280	98	62	ASCII String D	44	01000100
á	99	63	ASCII String i	69	01101001
20	100	64	ASCII String s	73	01110011
Tini	101	65	ASCII String p	70	01110000
	102	66	ASCII String 1	6C	01101100
	103	67	ASCII String a	61	01100001
	104	68	ASCII String y	79	01111001
	105	69	Manufacturer P/N(If<13 char> 0Ah, then terminate with ASC II code 0Ah,set remainin	0A	00001010
	106	6A	Manufacturer P/N(If<13 char> 0Ah, then terminate with ASC II code 0Ah,set remainin	20	00100000
	107	6B	Manufacturer P/N(If<13 char> 0Ah, then terminate with ASC Ⅱ code 0Ah,set remainin	20	00100000

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APPENDIX A. Enhanced Extended Display Identification Data (EEDID™) 3/3

	Byte (Dec)	Byte (Hex)	Field Name and Comments	Value (Hex)	Value (Bin)
	108	6C	Flag	00	00000000
	109	6D	Flag	00	00000000
	110	6E	Flag	00	00000000
	111	6F	Data Type Tag (ASCII String)	FE	11111110
	112	70	Flag	00	00000000
Tining Descriptor #4	113	71	ASCII String L	4C	01001100
<i>.</i> 0.	114	72	ASCII String P	50	01010000
ipa	115	73	ASCII String 1	31	00110001
SCI	116	74	ASCII String 5	35	00110101
Ž	117	75	ASCII String 6	36	00110110
20	118	76	ASCII String W	57	01010111
ı i i	119	77	ASCII String H	48	01001000
	120	78	ASCII String 3	33	00110011
	121	79	ASCII String -	2 D	00101101
	122	7A	ASCII String T	54	01010100
	123	7B	ASCII String L	4C	01001100
	124	7C	ASCII String S	53	01010011
	125	7D	ASCII String 1	31	00110001
Onecks	126	7E	Extension flag (# of optional 128 panel ID extension block to follow, Typ = 0)	00	00000000
	127	7 F	Check Sum (The 1-byte sum of all 128 bytes in this panel ID block shall = 0)	54	01010100

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